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AMENDMENT UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
GROUP 2815
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q63936

Makoto NONAKA

Appln. No.: 09/823,752

Group Art Unit: 2815

Confirmation No.: 9106

Examiner: George C. ECKERT II

Filed: April 03, 2001

For: SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME

AMENDMENT UNDER 37 C.F.R. § 1.116

ATTN: BOX AF
Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated November 21, 2003, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims 1, 2, 4-7, 22, 23 and 27-29 without prejudice or disclaimer.

Please enter the following amended claims:

1. (Amended) A semiconductor device having a plurality of wiring layers in a multi-layered structure,

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the semiconductor device including an inner area (11) at a surface and a pad area (13) surrounding the inner area (11) therein,

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